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JAN 04 2005

PATENT APPLICATION

ATTORNEY DOCKET NO. CSCO-0094342

**IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE**

Inventor(s): Balaji Lakshmikanth Bangalae, Sudhakar Shenoy and Shiju Joseph

Serial No.: 09/924,722

Examiner: NGUYEN, CHAU T

Filing Date: 8/9/2001

Group Art Unit: 2663

Title: Reducing Overhead When Using Loopback Cells for Fault Detection in Bi-Directional Virtual Circuits

**Honorable Commissioner for Patents
Alexandria, Virginia 22313-1450**

STATUS INQUIRY

Sir:

1. More than 40 months have passed since:

[X] NEW APPLICATIONS

the filing of this application on 8/9/2001. No communication has been received from Patent and Trademark Office indicating action on this application.

[] AMENDED APPLICATIONS

the filing of a response on _____. No further communication has been received from the Patent and Trademark Office.

[] APPEALED APPLICATION

The Appeal Brief was filed on _____.

[] An Examiner's Answer was mailed on _____.

[] A Reply Brief to the Examiner's Answer was submitted on _____.

[] ALLOWED APPLICATIONS

the mailing of Form POL-327 and/or Examiner's Amendment on _____.

2. Kindly advise the undersigned of the present status of this application, by checking the appropriate box on the next page.

Respectfully submitted,

By Naren Thappeta
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PATENT APPLICATION

ATTORNEY DOCKET NO. CSCO-009/4342

STATUS INQUIRY REPLY

Please include expected date of first office action

BY FACSIMILE TO: (707) 356-4172

APPLICATION SERIAL NO. 09/924,722 is currently

☐ Assigned to Group _____ and awaits:

☐ Action by the examiner

☐ Applicant's response to Office Action mailed _____

APPEAL NO. _____

☐ Awaiting action by the board of Appeals and Interferences

☐ Date of hearing expected _____

☐ Decision expected _____